# TCT 0603, TCU 0805, TCA 1206

Vishay Beyschlag

# **Trimmable Thick Film Flat Chip Resistors**



TCT 0603, TCU 0805, and TCA 1206 trimmable flat chip resistors are best suited whenever stable circuit adjustment is required and potentiometers will be either too expensive, too unstable or too large. The trimming is done directly on the printed-circuit board (PCB) using a state of the art laser trimming system e.g. with YAG or CO<sub>2</sub> laser source. Typical applications include any type of electronic sensors, oscillators or electronic circuits which have to be trimmed to certain functional parameters after PCB assembly.

### FEATURES

- Designed for state of the art laser trimming
- Enables economical functional circuit adjustment
- Low TCR ± 50 ppm/K available
- Excellent stability  $\leq \pm 0.25$  % (1000 h rated power at 70 °C)
- Wide ohmic range: 10  $\Omega$  to 1  $M\Omega$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### APPLICATIONS

- Electronic sensors
- Oscillators
- Electronic circuits

TECHNICAL SPECIFICATIONS			
DESCRIPTION	TCT 0603	TCU 0805	TCA 1206
Imperial size	0603	0805	1206
Metric size code	RR1608M	RR2012M	RR3216M
Resistance range	10 $\Omega$ to 1 M $\Omega$	10 $\Omega$ to 1 M $\Omega$	10 $\Omega$ to 1 M $\Omega$
Resistance tolerance	0 % / -30 %; 0 % / -20 %; 0 % / -10 %	0 % / -30 %; 0 % / -20 %; 0 % / -10 %	0 % / -20 %
Temperature coefficient	± 100 ppm/K; ± 50 ppm/K	± 100 ppm/K; ± 50 ppm/K	± 100 ppm/K
Rated dissipation, P <sub>70</sub> <sup>(1)</sup>	0.125 W	0.200 W	0.250 W
Operating voltage, U <sub>max.</sub> AC <sub>RMS</sub> /DC	75 V	150 V	200 V
Permissible film temperature, $\mathcal{P}_{F max.}$ <sup>(1)</sup>	155 °C		
Operating temperature range (1)	-55 °C to 155 °C		
Permissible voltage against ambient (insulation):			
1 min; U <sub>ins</sub>	100 V	200 V	300 V
Failure rate: FIT <sub>observed</sub>		≤ 0.1 x 10 <sup>-9</sup> /h	

### Note

<sup>(1)</sup> Please refer to APPLICATION INFORMATION below.

## **APPLICATION INFORMATION**

When the resistor dissipates power, a temperature rise above the ambient temperature occurs, dependent on the thermal resistance of the assembled resistor together with the printed circuit board. The rated dissipation applies only if the permitted film temperature is not exceeded.

These resistors do not feature a limited lifetime when operated within the permissible limits. However, resistance value drift increasing over operating time may result in exceeding a limit acceptable to the specific application, thereby establishing a functional lifetime.

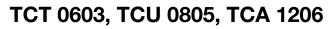
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(Pb) RoHS

COMPLIANT





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MAXIMUM RESISTANCE CHANGE AT RATED DISSIPATION						
OPERATION MODE		STANDARD	POWER			
	TCT 0603	0.100 W	0.125 W			
Rated dissipation, P70	TCU 0805	0.125 W	0.200 W			
	TCA 1206	0.250 W	-			
Operating temperature range	-55 °C to 125 °C	-55 °C to 155 °C				
Permissible film temperature, $\theta_{\rm F}$ max.		125 °C	155 °C			
	TCT 0603	10 Ω t	ο 1 ΜΩ			
	TCU 0805	10 $\Omega$ to 1 M $\Omega$				
Max. resistance change at $P_{70}$ for resistance range,	TCA 1206	10 Ω t	o 1 MΩ			
$ \Delta R/R $ after:	1000 h	≤ 0.25 %	$\leq 0.5$ %			
	8000 h	$\leq 0.5~\%$	≤ <b>1.0</b> %			
	225 000 h	≤ <b>1.5</b> %	-			

Note

The presented operation modes do not refer to different types of resistors, but actually show examples of different loads, that lead to
different film temperatures and different achievable load-life stability (drift) of the resistance value. A suitable low thermal resistance of the
circuit board assembly must be safeguarded in order to maintain the film temperature of the resistors within the specified limits. Please
consider the application note "Thermal Management in Surface-Mounted Resistor Applications" (www.vishay.com/doc?28844) for
information on the general nature of thermal resistance.

TEMPERATURE COEFFICIENT AND RESISTANCE RANGE					
TYPE / SIZE	TCR	TOLERANCE	RESISTANCE	E-SERIES	
		0 % / -30 %	10 Ω to 1 MΩ		
	± 100 ppm/K	0 % / -20 %	10 Ω to 1 MΩ		
TCT 0603		0 % / -10 %	10 $\Omega$ to 1 M $\Omega$		
		0 % / -30 %	100 $\Omega$ to 1 M $\Omega$		
	± 50 ppm/K	0 % / -20 %	100 Ω to 1 MΩ		
		0 % / -10 %	100 $\Omega$ to 1 M $\Omega$		
		0 % / -30 %	10 $\Omega$ to 1 M $\Omega$	E24	
	± 100 ppm/K	0 % / -20 %	10 Ω to 1 MΩ		
TCU 0805		0 % / -10 %	10 $\Omega$ to 1 M $\Omega$		
100 0805		0 % / -30 %	100 $\Omega$ to 1 M $\Omega$		
	± 50 ppm/K	0 % / -20 %	100 Ω to 1 MΩ		
		0 % / -10 %	100 $\Omega$ to 1 M $\Omega$		
TCA 1206	± 100 ppm/K	0 % / -20 %	10 $\Omega$ to 1 M $\Omega$		

Note

Resistance ranges printed in bold are preferred TCR/tolerance combinations with optimized availability.

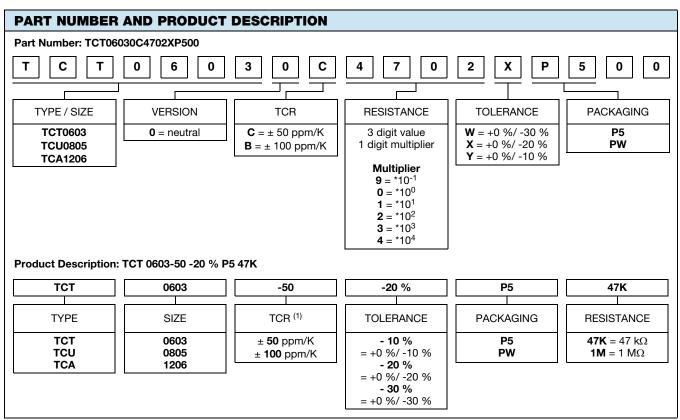
PACKAGING						
TYPE / SIZE	CODE	QUANTITY	PACKAGING STYLE	WIDTH	РІТСН	PACKAGING DIMENSIONS
TCT 0603	P5	5000		8 mm	4 mm	Ø 180 mm/7"
	PW	20 000			4 11111	Ø 330 mm/13"
TCU 0805	P5	5000	Paper tape acc. IEC 60286-3, Type 1a	8 mm	4 mama	Ø 180 mm/7"
	PW	20 000			4 mm -	Ø 330 mm/13"
TCA 1206	P5	5000		8 mm	4 mm	Ø 180 mm/7"

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2 For technical questions, contact: <u>thinfilmchip@vishav.com</u> Document Number: 28711

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Notes

• Products can be ordered using either the PART NUMBER or PRODUCT DESCRIPTION.

<sup>(1)</sup> A temperature coefficient  $\pm$  100 ppm/K is marked -00.



### DESCRIPTION

Production is strictly controlled and follows an extensive set of instructions established for reproducibility. A newly developed cermet layer is deposited onto a high-grade ceramic substrate ( $Al_2O_3$ ) and conditioned to achieve the desired temperature coefficient. Pre-contacts are built on both sides of the substrate. The resistor elements are covered by glass for superior electrical, mechanical and climatic protection. The terminations receive a final pure matte tin on nickel plating.

The result of the determined production is verified by an extensive testing procedure performed on 100 % of the individual chip resistors. Only accepted products are laid directly into the paper tape in accordance with **IEC 60286-3 Type 1a** <sup>(1)</sup>.

### ASSEMBLY

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using wave, reflow or vapor phase as shown in **IEC 61760-1** <sup>(1)</sup>. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions. The suitability of conformal coatings, potting compounds and their processes, if applied, shall be qualified by appropriate means to ensure the long-term stability of the whole system.

The resistors are RoHS-compliant; the pure matte tin plating provides compatibility with lead (Pb)-free and

lead-containing soldering processes. Solderability is specified for 2 years after production or requalification. The permitted storage time is 20 years. The immunity of the plating against tin whisker growth has been proven under extensive testing.

### MATERIALS

Vishay acknowledges the following systems for the regulation of hazardous substances:

- IEC 62474, Material Declaration for Products of and for the Electrotechnical Industry, with the list of declarable substances given therein <sup>(2)</sup>
- The Global Automotive Declarable Substance List (GADSL) (3)
- The REACH regulation (1907/2006/EC) and the related list of substances with very high concern (SVHC) <sup>(4)</sup> for its supply chain

The products do not contain any of the banned substances as per IEC 62474, GADSL, or the SVHC list, see www.vishay.com/how/leadfree.

Hence the products fully comply with the following directives:

- 2000/53/EC End-of-Life Vehicle Directive (ELV) and Annex II (ELV II)
- 2011/65/EU Restriction of the Use of Hazardous Substances Directive (RoHS) with amendment 2015/863/EU
- 2012/19/EU Waste Electrical and Electronic Equipment Directive (WEEE)

Vishay pursues the elimination of conflict minerals from its supply chain, see the Conflict Minerals Policy at www.vishay.com/doc?49037.

### APPROVALS

The resistors are tested in accordance with **EN 140401-802** which refers to **EN 60115-1**, **EN 60115-8**, and the variety of environmental test procedures of the **IEC 60068** <sup>(1)</sup> series.

Vishay Beyschlag has achieved "Approval of Manufacturer" in accordance with IECQ 03-1. The release certificate for "Technology Approval Schedule" in accordance with CECC 240001 based on IECQ 03-3-1 is granted for the Vishay Beyschlag manufacturing process.

### Notes

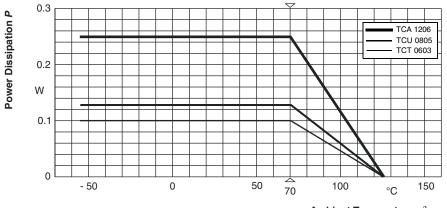
- <sup>(1)</sup> The quoted IEC standards are also released as EN standards with the same number and identical contents.
- (2) The IEC 62474 list of declarable substances is maintained in a dedicated database, which is available at <u>http://std.iec.ch/iec62474</u>.
- <sup>(3)</sup> The Global Automotive Declarable Substance List (GADSL) is maintained by the American Chemistry Council and available at <u>www.gadsl.org</u>.
- <sup>(4)</sup> The SVHC list is maintained by the European Chemical Agency (ECHA) and available at <u>http://echa.europa.eu/candidate-list-table</u>.

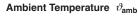
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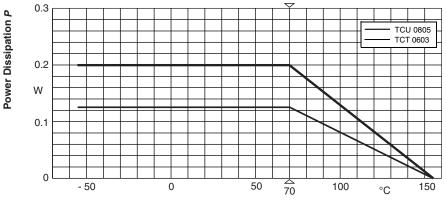
## FUNCTIONAL PERFORMANCE

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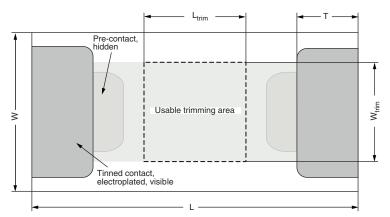






Ambient Temperature  $\vartheta_{amb}$ 





### Permissible Trimming Area

DIMENSIONS OF THE PERMISSIBLE TRIMMING AREA in millimeters							
TYPE / SIZE	L	W	Т	L <sub>trim</sub>	W <sub>trim</sub>		
TCT 0603	$1.55 \pm 0.05$	0.85 ± 0.1	0.3 + 0.15 / - 0.2	0.5	0.5		
TCU 0805	2.0 ± 0.1	1.25 ± 0.15	0.4 + 0.1 / - 0.2	0.8	0.8		
TCA 1206	3.2 + 0.1/- 0.2	1.6 ± 0.15	$0.5 \pm 0.25$	1.4	1.0		

Note

Please consider the application note "Using Laser Trimmable Resistors" at the link <u>www.vishay.com/doc?28893</u> for information on the trimming process and achievable trimming factors.

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TCT 0603, TCU 0805, TCA 1206



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## **TESTS AND REQUIREMENTS**

All tests are carried out in accordance with the following specifications:

EN 60115-1, generic specification

EN 60115-8 (successor of EN 140400), sectional specification

EN 140401-802, detail specification

IEC 60068-2-xx, test methods

The parameters stated in the Test Procedures and Requirements table are based on the required tests and permitted limits of EN 140401-802. The table presents only the most important tests, for the full test schedule refer to the documents listed above. However, some additional tests and a number of improvements against those minimum requirements have been included.

The testing also covers most of the requirements specified by EIA/ECA-703 and JIS-C-5201-1.

The tests are carried out under standard atmospheric conditions in accordance with IEC 60068-1, 4.3, whereupon the following values are applied:

Temperature: 15 °C to 35 °C

Relative humidity: 25 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar).

A climatic category LCT / UCT / 56 is applied, defined by the lower category temperature (LCT), the upper category temperature (UCT), and the duration of exposure in the damp heat, steady state test (56 days).

The components are mounted for testing on printed circuit boards in accordance with EN 60115-8, 2.4.2, unless otherwise specified.

TEST PROCEDURES AND REQUIREMENTS <sup>(1)</sup>						
EN 60115-1 CLAUSE	IEC 60068-2 <sup>(2)</sup> TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (\_\P_R)		
			Stability for product types:			
			TCT 0603	10 Ω to 1 MΩ		
			TCU 0805	10 Ω to 1 MΩ		
			TCA 1206	10 Ω to 1 MΩ		
4.5	-	Resistance		+0 % / -30 % <i>R</i> ; +0 % / -20 % <i>R</i> ; +0 % / -10 % <i>R</i>		
4.8	-	Temperature coefficient	At (20 / -55 / 20) °C and (20 / 125 / 20) °C	± 100 ppm/K; ± 50 ppm/K		
	-	Endurance at 70 °C: standard operation mode	$U = \sqrt{P_{70} \times R} \text{ or } U = U_{\text{max.}};$ whichever is the less severe; 1.5 h on; 0.5 h off; 70 °C; 1000 h	± (0.25 % <i>R</i> + 0.05 Ω)		
4.25.1			70 °C; 8000 h	$\pm$ (0.5 % R + 0.05 Ω)		
4.20.1	-	Endurance at 70 °C: power operation mode	$U = \sqrt{P_{70} \times R} \text{ or } U = U_{\text{max.}};$ whichever is the less severe; 1.5 h on; 0.5 h off;			
		power operation mode	70 °C; 1000 h	$\pm$ (0.5 % R + 0.05 Ω)		
			70 °C; 8000 h	± (1 % <i>R</i> + 0.05 Ω)		
4.25.3		Endurance at upper category	125 °C; 1000 h	± (0.25 % <i>R</i> + 0.05 Ω)		
4.20.0	-	temperature	155 °C; 1000 h	± (0.5 % <i>R</i> + 0.05 Ω)		
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (0.25 % <i>R</i> + 0.05 Ω)		

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EN 60115-1 CLAUSE	IEC 60068-2 <sup>(2)</sup> TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (\_R)
			Stability for product types:	
			TCT 0603	10 $\Omega$ to 1 M $\Omega$
			TCU 0805	10 $\Omega$ to 1 M $\Omega$
			TCA 1206	10 $\Omega$ to 1 M $\Omega$
4.23		Climatic sequence: standard operation mode:		
4.23.2	2 (Bb)	dry heat	125 °C; 16 h	
4.23.3	30 (Db)	damp heat, cyclic	55 °C; 24 h; > 90 % RH; 1 cycle	
4.23.4	1 (Ab)	cold	-55 °C; 2 h	± (0.25 % <i>R</i> + 0.05 Ω)
4.23.5	13 (M)	low air pressure	8.5 kPa; 2 h; (25 ± 10) °C	
4.23.6	30 (Db)	damp heat, cyclic	55 °C; 24 h; > 90 % RH; 5 cycles	
4.23.7	-	DC load	$U = \sqrt{P_{70} \times R} \le U_{\text{max.}}; \text{ 1 min.}$	
-	1 (Aa)	Cold	-55 °C; 2 h	$\pm (0.25 \% R + 0.05 \Omega)$
4.19 14 (Na)		Rapid change of temperature	30 min at LCT and 30 min at UCT; LCT = -55 °C; UCT = 125 °C; 5 cycles	± (0.25 % <i>R</i> + 0.05 Ω) no visible damage
			LCT = -55 °C; UCT = 125 °C; 1000 cycles	± (0.5 % <i>R</i> + 0.05 Ω) no visible damage
4.13	_	Short time overload: standard operation mode	$U = 2.5 \times \sqrt{P_{70} \times R}$ or $U = 2 \times U_{max.};$	± (0.25 % <i>R</i> + 0.05 Ω)
4.10		Short time overload: power operation mode	whichever is the less severe; 5 s	± (0.5 % <i>R</i> + 0.05 Ω)
4.22	6 (Fc)	Vibration	Endurance by sweeping; 10 Hz to 2000 Hz; no resonance; amplitude ≤ 1.5 mm or ≤ 200 m/s <sup>2</sup> ; 7.5 h	± (0.25 % <i>R</i> + 0.05 Ω); no visible damage
		Solderability	Solder bath method; SnPb40; non-activated flux (215 ± 3) °C; (3 ± 0.3) s	Good tinning (≥ 95 % covered);
4.17	58 (Td)	(Td) Solderability Solder bath method; SnAg3Cu0.5 or SnAg3.5; non-activated flux (235 ± 3) °C; (2 ± 0.2) s		no visible damage
4.18	58 (Td)	Resistance to soldering heat	Solder bath method; (260 $\pm$ 5) °C; (10 $\pm$ 1) s	± (0.25 % <i>R</i> + 0.05 Ω); no visible damage
4.29	45 (XA)	Component solvent resistance	Isopropyl alcohol +50 °C; method 2	No visible damage
4.32	21 (Ue <sub>3</sub> )	Shear (adhesion)	TCT 0603: 9 N TCU 0805 and TCA 1206: 45 N	No visible damage
4.33	21 (Ue <sub>1</sub> )	Substrate bending	Depth 2 mm, 3 times	$\pm$ (0.25 % R + 0.05 Ω) no visible damage, no open circuit in bent positio
4.7	-	Voltage proof	$U_{\rm RMS} = U_{\rm ins};$ (60 ± 5) s	No flashover or breakdown
4.35	-	Flammability	IEC 60695-2-2 <sup>(2)</sup> , needle flame test; 10 s	No burning after 30 s

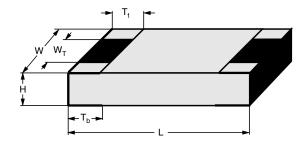
### Notes

<sup>(1)</sup> All given figures are valid for the untrimmed resistor.

<sup>(2)</sup> The quoted IEC standards are also released as EN standards with the same number and identical contents.

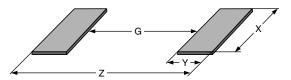


### DIMENSIONS



DIMENSIONS AND MASS								
TYPE / SIZE	H (mm)	L (mm)	W (mm)	W <sub>T</sub> (mm)	T <sub>t</sub> (mm)	Т <sub>ь</sub> (mm)	MASS (mg)	
TCT 0603	0.45 + 0.1 / - 0.05	1.55 ± 0.05	0.85 ± 0.1	> 75 % of W	0.3 + 0.15 / - 0.2	0.3 + 0.15 / - 0.2	1.9	
TCU 0805	0.45 + 0.1 / - 0.05	2.0 ± 0.1	1.25 ± 0.15	> 75 % of W	0.4 + 0.1 / - 0.2	0.4 + 0.1 / - 0.2	4.6	
TCA 1206	0.55 ± 0.1	3.2 + 0.1/- 0.2	1.6 ± 0.15	> 75 % of W	0.5 ± 0.25	0.5 ± 0.25	9.2	

### SOLDER PAD DIMENSIONS



RECOMMENDED SOLDER PAD DIMENSIONS								
		WAVE SO	LDERING		REFLOW SOLDERING			
TYPE / SIZE	G (mm)	Y (mm)	X (mm)	Z (mm)	G (mm)	Y (mm)	X (mm)	Z (mm)
TCT 0603	0.55	1.10	1.10	2.75	0.65	0.70	0.95	2.05
TCU 0805	0.80	1.25	1.50	3.30	0.90	0.90	1.40	2.70
TCA 1206	1.40	1.50	1.90	4.40	1.50	1.15	1.75	3.80

### Notes

• The given solder pad dimensions reflect the considerations for board design and assembly as outlined e.g. in standards IEC 61188-5-x<sup>(1)</sup>, or in publication IPC-7351.

<sup>(1)</sup> The quoted IEC standards are also released as EN standards with the same number and identical contents.

# TCT 0603, TCU 0805, TCA 1206



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## **12NC INFORMATION FOR HISTORICAL CODING REFERENCE ONLY**

- The resistors have a 12 digit numeric code starting with 2312.
- The subsequent 4 digits indicate the resistor type, specification and packaging; see the 12NC table.
- The remaining 4 digits indicate the resistance value:
- The first 3 digits indicate the resistance value.
- The last digit indicates the resistance decade. In accordance with the 12NC indicating resistance decade table.

### Last digit of 12NC indicating resistance decade

RESISTANCE DECADE	LAST DIGIT
10 Ω to 99.9 Ω	9
100 $\Omega$ to 999 $\Omega$	1
1 kΩ to 9.99 kΩ	2
10 kΩ to 99.9 kΩ	3
100 kΩ to 999 kΩ	4
1 M $\Omega$ to 9.99 M $\Omega$	5

### **12NC EXAMPLE**

The 12NC of a TCT 0603 resistor, value 47 k $\Omega$  and TCR 50 with +0 % / -20 % tolerance, supplied in cardboard tape of 5000 units per reel is: 2312 300 64703.

<b>12NC -</b> R	esistor type a	and packagir	ng				
DESCRIPTIO			CODE 2312				
DESCRIPTIC	JN		CARDBOARD 1	TAPE ON REEL			
ТҮРЕ	TCR	TOL.	P5 5000 UNITS	PW 20 000 UNITS			
		+0 % / -30 %	300 1	305 1			
	± 100 ppm/K	+0 % / -20 %	300 2	305 2			
TCT 0603		+0 % / -10 %	300 3	305 3			
101 0603		+0 % / -30 %	300 5	305 5			
	± 50 ppm/K	+0 % / -20 %	300 6	305 6			
		+0 % / -10 %	300 7	305 7			
		+0 % / -30 %	320 1	325 1			
	± 100 ppm/K	+0 % / -20 %	320 2	325 2			
TCU 0805		+0 % / -10 %	320 3	325 3			
100 0805		+0 % / -30 %	320 5	325 5			
	± 50 ppm/K	+0 % / -20 %	320 6	325 6			
		+0 % / -10 %	320 7	325 7			
TCA 1206	± 100 ppm/K	+0 % / -20 %	340 2	345 2			



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